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## U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AA		4,612,629	09/16/86	Harari			
AB		4,785,342	11/15/88	Yamanaka et al.			
AC		5,786,249	07/28/98	Dennison			
AD		6,074,924	06/13/00	Dennison et al.			
AE		5,272,367	12/21/93	Dennison et al.			
AF		6,004,854	12/21/99	Dennison et al.			
AG		5,683,927	11/04/97	Dennison et al.			
AH		6,124,616	09/26/00	Dennison et al.			
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AK		5,7706,806	07/07/98	Dennison et al.			
AL		5,773,863	06/60/98	Burr et al.			

## FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AM	EP 0 717 448 A1	06/19/96	EPO	—	—		
AN	WO 98/29897	07/09/98	PCT	—	—		
AO							
AP							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

AQ		Silicon Processing for the VLSI Era, Vol. III; "Punchthrough (Subsurface-DIBL) in Short-Channel MOSFETs"; undated; pp. 232-235, 238-244
AR		
AS		

EXAMINER <i>James Soley</i>	DATE CONSIDERED <i>10/17/02</i>
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